

843.41127X00

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Applicant(s):

S. TANAKA, et al

Serial No.:

10/073,029

Filed:

February 12, 2002

For:

DIRECT-CONVERSION TRANSMITTING CIRCUIT AND

INTEGRATED TRANSMITTING/RECEIVING CIRCUIT

Group:

2816

Examiner:

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97 & 1.98

Mail Stop DD Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

May 28, 2003

Sir:

In the matter of the above-identified application, applicant(s) is/are submitting herewith a copy of a communication from a foreign patent office in a counterpart foreign application and copies of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted before the mailing date of a first office action on the merits and with the following certification specified in 37 CFR 1.97(e).

On information and belief, I hereby certify that each item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not than more than three months prior to the filing of this statement.

Each of the documents listed on the attached form equivalent to Form PTO-1449 is in the English language.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 01-2135 (843.41127X00) and please credit any excess fees to such deposit account.

Respectfully submitted,

Melvin Kraus

Registration No. 22,466

ANTONELLI, TERRY, STOUT & KRAUS, LLP

MK/cee Attachments (703) 312-6600

Form PTO-1449 Equivalent U.S. Department of Commerce Patent and Trademark Office

Atty. Docket No. 843.41127X00 Serial No. 10/073,029

Applicant: S. TANAKA, et al Filing Date: February 12, 2002

Group: 2816

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Other Documents (including Author, Title, Date, Pertinent Pages, etc.)

Examiner Date Considered

^{*}Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.





P.B.5818 - Patentlaan 2 2280 HV Rijswijk (ZH) 3 +31 70 340 2040 TX 31651 epo nl FAX +31 70 340 3016

Europäisches Patentamt

Zweigştelle in Den Haag Recherchenabteilung

European Patent Office

Branch at The Hague Search division

Office européen des brevets

Département à La Haye Division de la recherche

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RECORDS ENTD
RECORDS SEEN
DIARY ENT'D
RENEWAL ENT'D X
ALREADY ENT'D

Datum/Date 06.03.03

Zeichen/Hef./Réf.

C/FP5985973

Anmeldung Nr./Application No./Demande n°./Patent Nr./Patent No./Brevet n°.

02250529.1-2215-

Anmelder Applicant/Demandeur/Patentinhaber/Proprietor/Titulaire Hitachi Ltd., et al

COMMUNICATION

The European Patent Office herewith transmits as an enclosure the European search report for the above-mentioned European patent application.

If applicable, copies of the documents cited in the European search report are attached.

Additional set(s) of copies of the documents cited in the European search report is (are) enclosed as well.

The following specifications given by the applicant have been approved by the Search Division:

□ abstract

X title

The abstract was modified by the Search Division and the definitive text is attached to this communication.

The following figure will be published together with the abstract:

11

REFUND OF THE SEARCH FEE

If applicable under Article 10 Rules relating to fees, a separate communication from the Receiving Section on the refund of the search fee will be sent later.





EUROPEAN SEARCH REPORT

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EP 02 25 0529

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ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

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ABSTRACT / ZUSAMMENFASSUNG / ABREGE

02250529.1

A three band GSM (800MHz, 1800MHz, 1900MHz) transmitter/receiver comprises direct conversion mixers and one RF PLL synthesiser with a VCO. In order to reduce DC-offset, to reduce the number of parts and to achieve noise reduction in the receiving band, low-pass filters (130 and 131) are provided at IQ input sections of a modulator (106) that converts IQ signals into RF signals.

In comparison with a conventional transmitter using an offset PLL, an external VCO in addition to an RF integrated circuit, a power amplifier, and a front end are used. The circuit does not require an expensive and external high frequency filter such as a surface acoustic wave (SAW) filter or the like.